

Applicant: Andrew Holland  
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**IN THE CLAIMS:**

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A mounting for a semiconductor assembly ~~including~~ comprising:  
a first portion for mounting a semiconductor assembly[[,]];  
a second portion; and  
a connecting portion joining the first and second portions and arranged to allow folding of the second portion over the first portion to form a cover, wherein the mounting comprises a sealing material at least partially encapsulating the mounting and the semiconductor assembly such that at least part of a printed circuit board facing surface of the first portion and/or the heat radiating surface of the second portion are left exposed.
2. (Canceled).
3. (Currently Amended) A mounting according to claim 1 ~~or claim 2~~ wherein the first portion of the mounting comprises a formation of electrical connectors, which have said printed circuit board facing said surfaces, which are not covered by said sealing material.

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4. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the second portion is arranged to be in a spaced parallel relationship with the first portion.

5. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the second portion further comprises at least one additional edge portion arranged to extend when the mounting is folded beyond at least one edge of the first portion of the mounting.

6. (Original) A mounting according to claim 5 wherein the mounting is in the form of an EMI enhanced package wherein the second portion is provided with four additional edge portions to define four walls to protect the semiconductor assembly.

7. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting is formed from a single sheet of electrically and thermally conducting material which is preferably copper.

8. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the connecting portion is provided with folding means to enable the folding of the second portion over the first portion[[;]], and the folding means is preferably at least one weakened line, such as a scored line or an etched line in the mounting having a thickness that is less than that of the rest of the mounting[[;]], and more preferably the folding means includes

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two weakened lines, one between the first portion and the connecting portion and one between the second portion and the connecting portion.

9. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting is provided with a third portion and second folding portion arranged to allow folding of the third portion over the second portion to form ~~[[a]]~~said cover, preferably such that the third portion is in a spaced parallel relationship with the first portion and second portion.

10. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting further comprises a means for mounting surface mount technology (SMT) component which is preferably a passive component, for example a resistor, capacitor, and/or inductor.

11. (Original) A mounting according to claim 10 wherein the SMT mounting means comprises one or more recesses in the second portion.

12. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the cover is patterned or formed to function as a passive component which is preferably an antenna, an ~~serpentine~~ inductor, an interdigitated and/or parallel plate capacitor, a microstrip coupler and/or a filter.

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13. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting further comprises means adapted for mounting a sensor semiconductor assembly, preferably the sensor mounting means is adapted for mounting an image sensor semiconductor assembly, biometric sensor semiconductor assembly and/or pressure sensor semiconductor assembly.

14. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the cover is adapted to provide direct access to the semiconductor assembly, preferably such direct access means comprises an aperture in the cover.

15. (Original) A mounting according to claim 14 wherein the mounting is further adapted to mount an optical component in relationship to an image sensor semiconductor chip.

16. (Original) A mounting according to claim 14 wherein the direct access means is further defined by having one or more recesses about its perimeter, which recesses preferably face towards, or away from, a mounted semiconductor assembly.

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17. (Currently Amended) A mounting according to ~~any one of claim~~[[s]] 14 [[to 16]], wherein the direct access means and/or the recesses can be used to locate a further component for use in the semiconductor assembly.

18. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting further comprises one or more recesses formed within the cover into which mould material can flow to secure the cover.

19. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, which further comprises a means to permit coupling of selected frequencies of electromagnetic radiation through the mounting, preferably the frequency coupling means comprises one or more apertures in the cover of appropriate dimension to permit coupling at a selected frequency.

20. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the formation of electrical connectors is in a spaced relationship with the base support and are linked electrically with the semiconductor assembly.

21.-22. (Canceled).

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23. (Currently Amended) A mounting according to ~~any one of the preceding claim~~[[s]] 1, wherein the mounting further comprises a heat dissipation means to provide a low thermally resistive path between a mounted semiconductor assembly and the cover of the package.

24. (Currently Amended) A mounting according to ~~any one of claim~~[[s]] 1 [[to 19]], wherein the mounting is part of an array of a plurality of mountings.

25.-40. (Canceled).

41. (New) A mounting for a semiconductor assembly comprising:

a first portion for mounting a semiconductor assembly;

a second portion; and

a connecting portion joining said first portion and said second portion and arranged to allow folding of said second portion over said first portion to form a cover, wherein said cover is patterned or formed to function as a passive component.

42. (New) A mounting for a semiconductor assembly comprising:

a first portion for mounting a semiconductor assembly;

a second portion; and

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a connecting portion joining said first and said second portion and arranged to allow folding of said second portion over said first portion to form a cover, wherein said cover is adapted to provide direct access to the semiconductor assembly.